

STANDARD FREQUENCY COMPONENTS CERAMIC SMD SV5070-4CA, SV5070-6CA VCXO

Description

VCXO Ceramic SMD : SV5070-4CA, SV5070-6CA

Ceramic SMD Voltage Controlled Oscillator
1.000 MHz ~ 80.000 MHz frequency range
RoHS Compliant

Features

Compact, surface mountable package
Available in embossed tape & reel or tube magazine
Perfect for PCs, laptop, portable application
Low voltage CMOS, HCMOS and TTL compatible

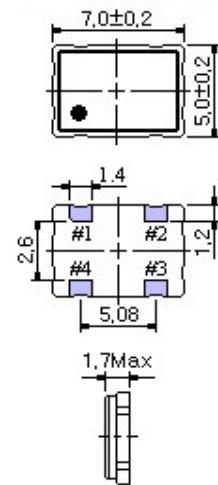


Electrical Specifications

Type:	VCXO (Voltage Controlled Oscillator)
Frequency Range:	1.000 MHz ~ 80.000 MHz
Operating Temp Range:	0°C ~ +70°C (Typ.)
Storage Temp Range:	-40°C ~ +85°C
Start Up Time:	10 m Sec Max.
Load Drive Capability:	15 pF HCMOS Load
Linearity:	± 20 % ~ ± 5 %
Frequency Stability:	± 10 ppm ~ ± 50 ppm Max.
Rise / Fall Time:	5 n Sec Max.
Aging at 25 °C:	± 3 ppm / Year Max.

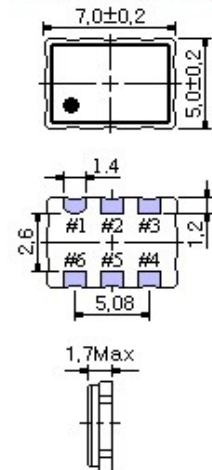
Parameters		Characteristics	
Supply Voltage (VDD)		5.0 VDC ± 5 %	3.3 VDC ± 5 %
Input Current	1.000 MHz ~ 20.000 MHz	20 mA Max.	15 mA Max.
	20.001 MHz ~ 40.000 MHz	30 mA Max.	25 mA Max.
	40.001 MHz ~ 60.000 MHz	40 mA Max.	35 mA Max.
	60.001 MHz ~ 80.000 MHz	50 mA Max.	45 mA Max.
Frequency Deviation /	2.5 VDC ± 2.0 VDC	± 10 ppm ~ ± 100 ppm Min	
Pin 1 Control Voltage	2.5 VDC ± 2.5 VDC	± 10 ppm ~ ± 150 ppm Min	
	1.65 VDC ± 1.65 VDC (or ± 1.5 VDC)	± 10 ppm ~ ± 150 ppm Min	
Output Voltage Logic High (VOH)	w/ HCMOS Load	90 % VDD Min.	
Output Voltage Logic Low (VOL)	w/ HCMOS Load	10 % of VDD Max.	
Duty Cycle	at 50 % of Waveform w/ HCMOS Load	50 ± 10 % (Typ.), 50 ± 5%	
6 PAD MODEL			
Pin 2 Tri-State	No Connection	Enable Output	
Input Voltage or Pin 5	VIH: ≥ 2.0 VDC	Enable Output	
Tri-State Input Voltage	VIL: ≤ 0.8 VDC	Disable Output: High Impedance	

SV5070-4CA



PAD CONFIGURATION
#1 VC
#2 GND
#3 OUTPUT
#4 VDD

SV5070-6CA



PAD CONFIGURATION
#1 VC #2 Tri-State
#3 GND #4 OUTPUT
#5 NC or Tri-State
#6 VDD

**CERAMIC SMD SV5070-4CA, SV5070-6CA
1.000 MHz ~ 80.000 MHz (VCXO'S)**