

A high quality, ceramic smd, crystal clock oscillator manufactured over the wide frequency range of 0.0216MHz to 125MHz. Low supply current, tight symmetry, supply voltage from 1.8Vd.c. to 5Vd.c.

A standard package providing an excellent combination of parameters within a small ceramic smd package.

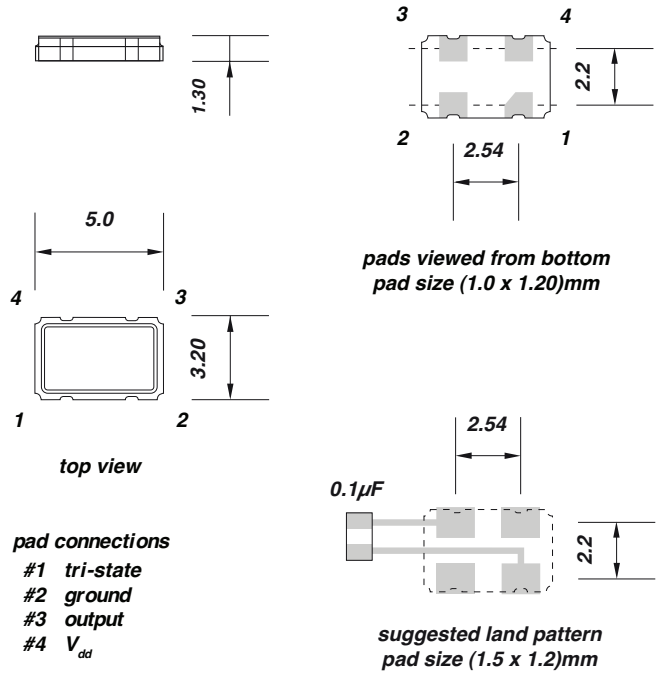
Supplied on tape and reel; 1000, 2000, 3000 or 5000 pieces per reel.

**Frequency stability -vs- temperature:**

TEMP. RANGE	COMBINED TOLERANCE		
	(-10 +60)°C	±20ppm	±25ppm
(-20 +70)°C	±20ppm	±25ppm	±50ppm
(-40 +85)°C		±25ppm	±50ppm

Tolerance inclusive of calibration tolerance at +25°C, temperature tolerance, load variation and supply voltage variation, first year ageing, vibration and shock

**Dimensions(mm)**



**Electrical specification:**

	5.0Vd.c.		3.3Vd.c.		2.5Vd.c.		1.8Vd.c.		
	min.	max.	min.	max.	min.	max.	min.	max.	
supply voltage ±10%	4.5	5.5	2.97	3.63	2.25	2.75	1.62	1.98	Vd.c.
frequency range	0.03456	75	0.0216	125	0.0216	125	0.0216	125	MHz
supply current (0.032768 ~ 0.6912)MHz	-	7	-	5	-	5	-	-	mA
supply current (0.6912 ~ 1.5)MHz	-	7	-	5	-	5	-	5	mA
supply current (1.5 ~ 20)MHz	-	10	-	7	-	7	-	5	mA
supply current (20 ~ 50)MHz	-	30	-	20	-	15	-	15	mA
supply current (50 ~ 70)MHz	-	40	-	30	-	20	-	15	mA
supply current (70 ~ 75)MHz	-	40	-	30	-	20	-	20	mA
supply current (75 ~ 100)MHz	-	-	-	30	-	25	-	20	mA
supply current (100 ~ 125)MHz	-	-	-	40	-	30	-	25	mA
CMOS o/p high	90% V <sub>DD</sub>		90% V <sub>DD</sub>		90% V <sub>DD</sub>		90% V <sub>DD</sub>		V
CMOS o/p low		10% V <sub>DD</sub>		10% V <sub>DD</sub>		10% V <sub>DD</sub>		10% V <sub>DD</sub>	V
tr (0.032768 ~ 0.3456)MHz	-	200	-	200	-	200	-	-	nano sec.
tr (0.3456 ~ 0.6912)MHz	-	5	-	6	-	6	-	-	nano sec.
t <sub>r</sub> (0.6912 ~ 20)MHz	-	5	-	6	-	6	-	6	nano sec.
t <sub>r</sub> (20 ~ 50)MHz	-	4	-	4	-	5	-	5	nano sec.
t <sub>r</sub> (50 ~ 70)MHz	-	2	-	3	-	3	-	3	nano sec.
t <sub>r</sub> (70 ~ 75)MHz	-	2	-	3	-	3	-	3	nano sec.
t <sub>r</sub> (75 ~ 125)MHz	-	-	-	2	-	2	-	3	nano sec.
start up time		5		5		5		5	milli sec.
tri-state: active o/p	0.8V <sub>DD</sub>	-	0.6V <sub>DD</sub>	-	0.7V <sub>DD</sub>	-	0.7V <sub>DD</sub>	-	V
tri-state: high impedance o/p	-	0.16V <sub>DD</sub>	-	0.15V <sub>DD</sub>	-	0.2V <sub>DD</sub>	-	0.27V <sub>DD</sub>	V
absolute clock period jitter	-	40	-	40	-	40	-	40	pico sec.
standby current	-	10	-	10	-	10	-	10	µA
ageing	-	±3	-	±3	-	±3	-	±3	ppm
storage temperature range									(-55 +125)°C