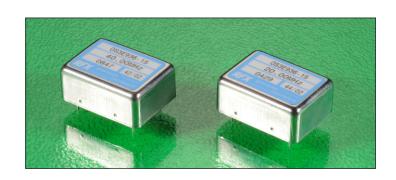


Stratum 3E compliant, GR-1244-CORE issue 2 and GR-63-CORE issue 1.

High quality, excellent phase noise, extremely low ageing from a precision SC cut resonator.

Manufactured to standard and custom frequencies 1.0Mz to 125MHz.



Star	ndai	rd o	ptic	ns:
			P	

supply voltage codes: $(V1)^*$ $(V2)^*$ $(V3)^*$ supply voltage+3.3Vd.c.+5.0Vd.c.+12.0Vd.c.trim reference option*+3.0Vd.c.+4.5Vd.c.+4.5Vd.c.

* add suffix (R) for V_{ref} output on pin #5

Generic specification:

output: CMOS 15pF, 45% ~ 55% rise and fall time 2ns max.

stability:

against temperature change stratum 3E compliant

against supply voltage change against load change ageing short term after 30 days continuous operation ageing long term voltage trim V_t trim input impedance

power supplies:

supply voltage V_{cc} start up current at min. temp. range quiescent current at max. temp. range warm up time insulation resistance

phase noise:

single sideband, 1Hz bandwidth

temperature: operating range storage range

 ± 0.0085 ppm(0 +70)°C long term and 24 hour holdover requirements of Stratum 3E levels specified in GR-1244-CORE issue 2 and GR-63-CORE issue 1 ± 0.002 ppm max. for V_{cc} $\pm 5\%$ ± 0.002 ppm max. for load $\pm 10\%$ ± 0.0005 ppm max. per day

> ±0.1ppm max. first year ±0.5ppm min. typical, linearity ±5% 100KΩ min.

> -110dBc/Hz, f_o+10Hz -135dBc/Hz, f_o+100Hz -155dBc/Hz, f_o+1kHz

> > (0 +70)°C (-40 +125)°C





Environmental conditions:

mechanical shock: MIL standard 202F, method 213, condition J thermal shock: MIL standard 202F, method 107, condition A vibration: MIL standard 202F, method 204, condition B

solderability: 5 seconds max. at +230°C, 3 seconds max. at +350°C

Marking: part number and frequency on high temperature

metalised polyester label

Ordering code: standard specification: OS3E936-15-V2* - 10.00M

OS3E936-15 = series generic code

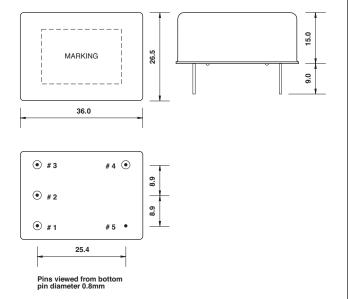
10.00M

V2* supply voltage code: V2 = +5Vd.c. supply

*Add suffix (R) for V_{ref} output on pin #5 output frequency: **10.00M = 10.000MHz**

Custom specification: part number issued with custom specification and drawing

Dimensions(mm):



Pin connections:

1 trim

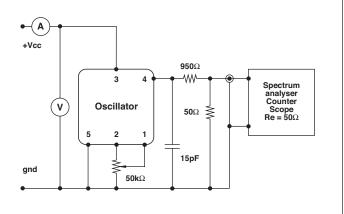
#2 n.c. or trim reference voltage*

#3 $+V_{cc}$

#4 output

#5 ground/case

Test circuit, CMOS load:



test circuit includes a 20:1 step down into a matched 50 Ω load